



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Application No.** 10/066,984

**Confirmation No.** 4187

**First Named Inventor:** Tse-Yu Yeh

**Filed:** 02/04/2002

**Art Unit:** 2183

**Examiner:** Rizzuto, Kevin P.

**Title:** Inhibiting of a co-issuing instruction in a processor  
having different pipeline lengths (as amended)

Mail Stop RCE  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

In response to the Final Office Action mailed June 15, 2005, please consider the following response with the filing of this RCE. Claims 1-6, 9-16, 19 and 20 are pending.

**CERTIFICATE OF MAILING**

37 C.F.R 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

9-15-05

Date

William W. Kief

Signature